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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,829	07/30/2003	Masanori Ishizuka	1614.1353	7282
21171 7590 11/03/2008 STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005			EXAMINER LEE, Y YOUNG	
			ART UNIT 2621	PAPER NUMBER
			MAIL DATE 11/03/2008	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/629,829

Applicant(s)

ISHIZUKA ET AL.

Examiner

Y. Lee

Art Unit

2621

Period for Reply -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 August 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1, 3-10 and 12 is/are pending in the application.
- 4a) Of the above claim(s) 6-10 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 3-5 and 12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 19 July 2007 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SF-08)
- Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
- Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Election/Restrictions

1. Claims 6-10 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected embodiment, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 3/2/07.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Drawings

3. The drawings were received on 7/19/07. These drawings are acceptable.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

6. Claims 1, 3-5, and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over AAPA in view of Yasuhiko (JP 09-294263).

AAPA, in Figures 1 and 2, discloses substantially the same data encoding/decoding apparatus as specified in claims 1, 3-5, and 12 of the present invention, comprising a decoder 16 decoding a coded stream, which is formed in a first format and inputted on real time, to generate video data and audio data; a video output memory 13 storing the video data from the decoder; an audio output memory 15 storing the audio data from the decoder; a video input memory 3 storing video data which is externally supplied to the data encoding/decoding apparatus (e.g. Video Input Signal); an audio input memory 5 storing audio data which is externally supplied to the data encoding/decoding apparatus (e.g. Audio Input Signal); a first data path (e.g. elements 13-12-2) and second data path (e.g. elements 15-14-4) provided to connect the decoder 16 to the input memories (3, 5), when the coded stream (e.g. input stream IN) of the first format is transcoded 6 to generate a second stream (e.g. encode stream OUT) formed in a second format; and an encoder 6 encoding the video data from the video input memory and the audio data from the audio input memory to generate the second stream of the second format, wherein when the transcoding is performed, the first data path and the second data path are set from OFF state to ON state, so that the video data output from the decoder is stored in the video input memory 3 through the first data path and delivered to the encoder 6 after passing through the video input memory 3, and the audio data output from the decoder is stored in the audio input memory 5 through the second data path and delivered directly to the encoder after passing through the audio input memory 5.

With respect to claims 3-5 and 12, AAPA also discloses that a video output interface 12 outputting the video data stored in the video output memory to an external device in a predetermined format at predetermined times; and an audio output interface 14 outputting the audio data stored in the audio output memory to an exterior device in a predetermined format at predetermined times; a video input interface 2 storing in the video input memory video data which is inputted from an external device at predetermined times; and an audio input interface 4 storing in the audio input memory audio data which is inputted from an external device at predetermined times; a clock generating unit 8 generating a clock signal for circuit components of the data encoding/decoding apparatus wherein the clock signal from the clock generating unit is supplies to each circuit component without adjusting a phase of the clock signal based on clock reference information of the coded stream inputted on real time (Fig. 2).

It is noted AAPA differs from the present invention in that it fails to particularly disclose the decoder directly connected to the input memories and the data are passing through them only as specified in claims 1, 3-5, and 12. Yasuhiko however, in Figure 1, teaches the concept of such arrangement in an image information compressor without adjusting the phase of the clock signal, wherein the decoded data (e.g. 19-24) is directly delivered to the encoder (e.g. 7-17) after passing through the video input memory 6 only.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made, having both the references of AAPA and Yasuhiko before him/her, to exploit the well known transcoding set up as taught by Yasuhiko in the data encoding/decoding apparatus of AAPA in order to prevent the image quality of an image after expansion from being

deteriorated gradually due to quantization noise and provide an improved real-time performance in processing video data streams.

Response to Arguments

7. Applicant's arguments with respect to claims 1, 3-5, and 12 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

8. Applicant's submission of an information disclosure statement under 37 CFR 1.97(c) with the fee set forth in 37 CFR 1.17(p) on 10/6/08 prompted the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 609.04(b). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Y. Lee whose telephone number is (571) 272-7334. The examiner can normally be reached on (571) 272-7334.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Miller can be reached on (571) 272-7353. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Young Lee/
Primary Examiner
Art Unit 2621

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